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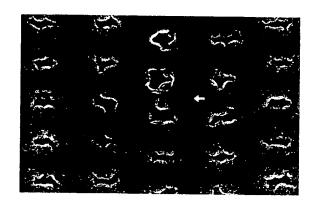
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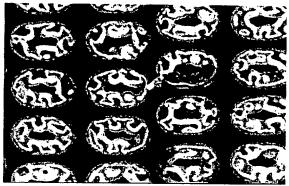


FIG. 1A

FIG. 1B

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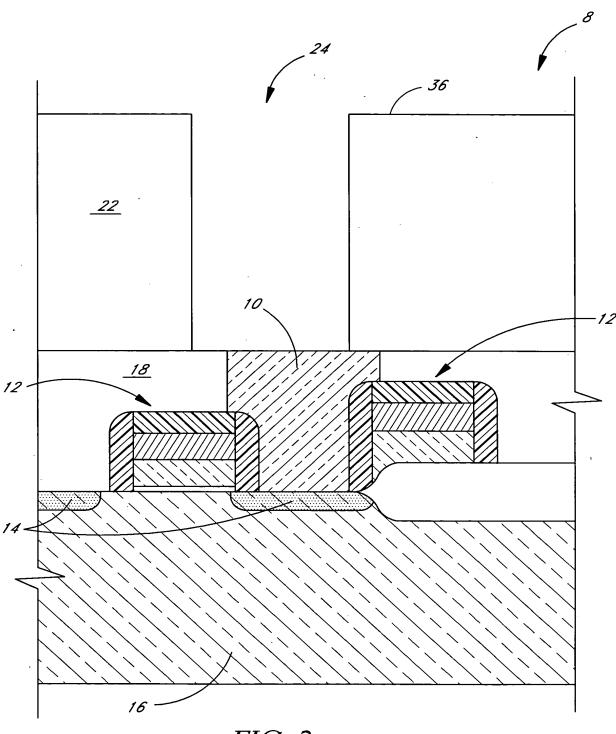
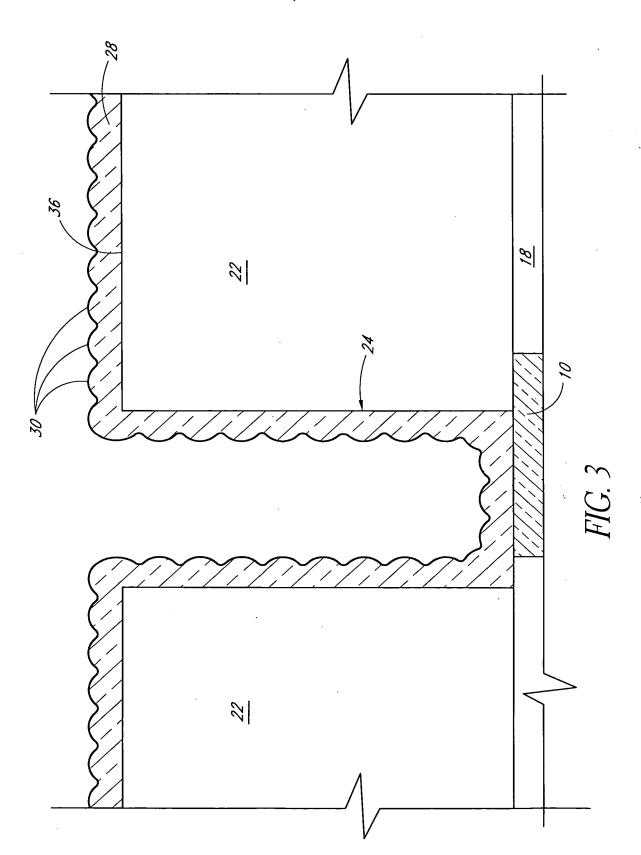


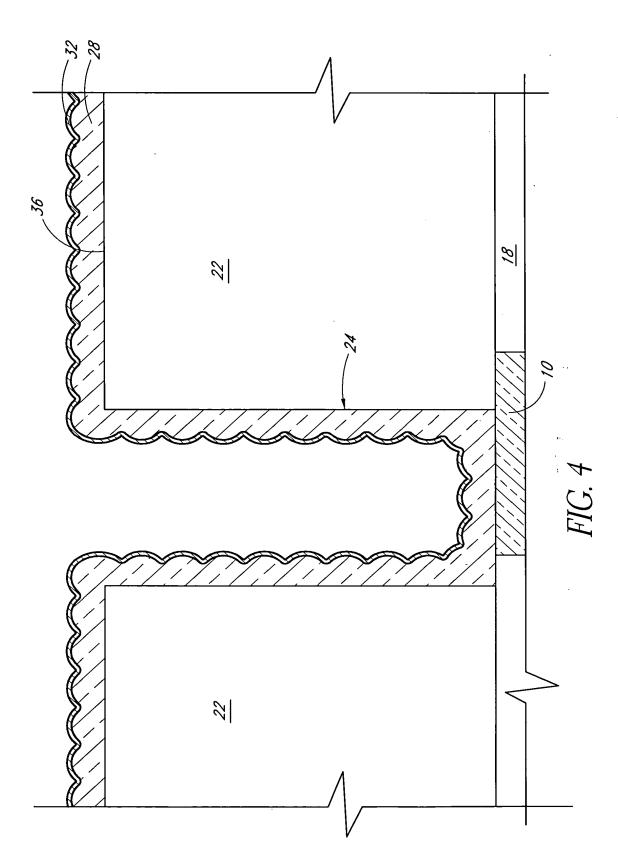
FIG. 2

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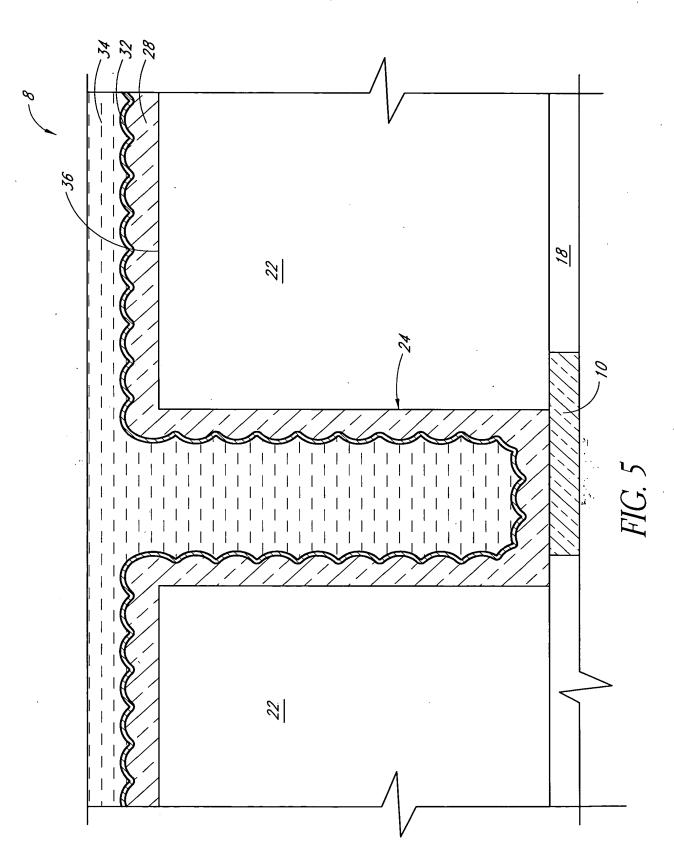
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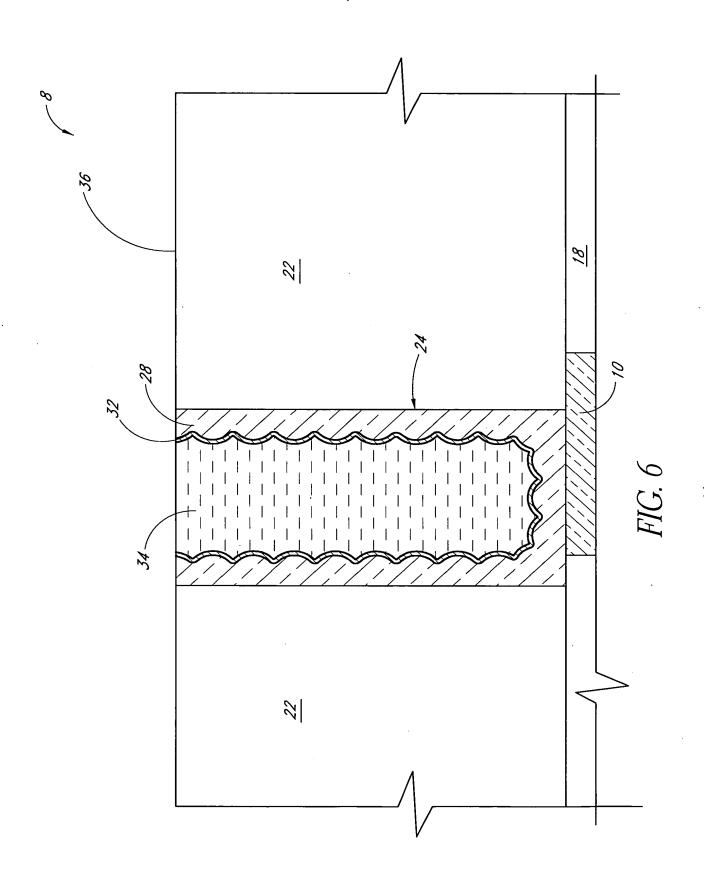
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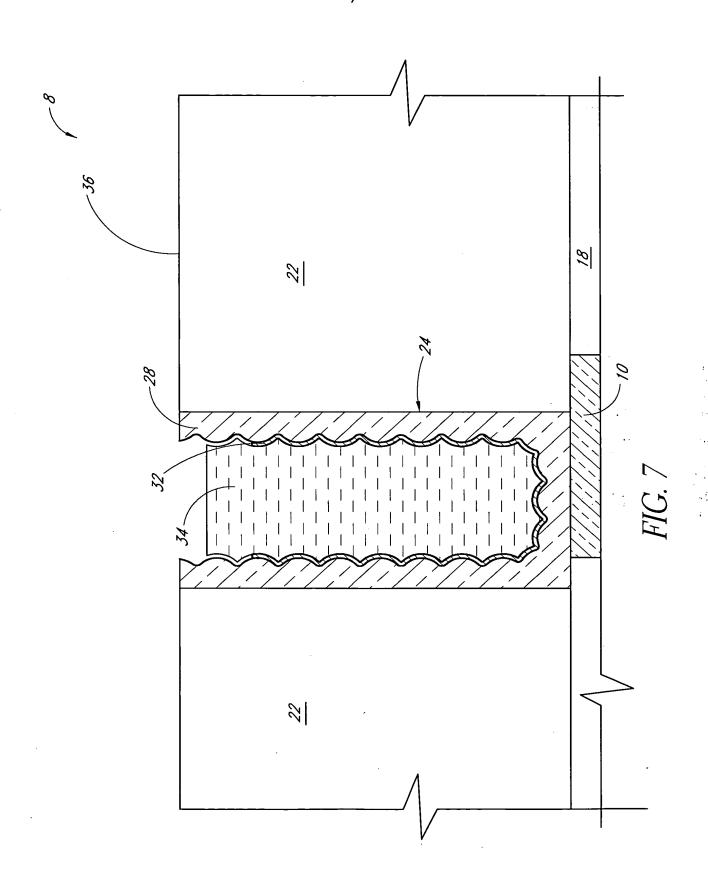


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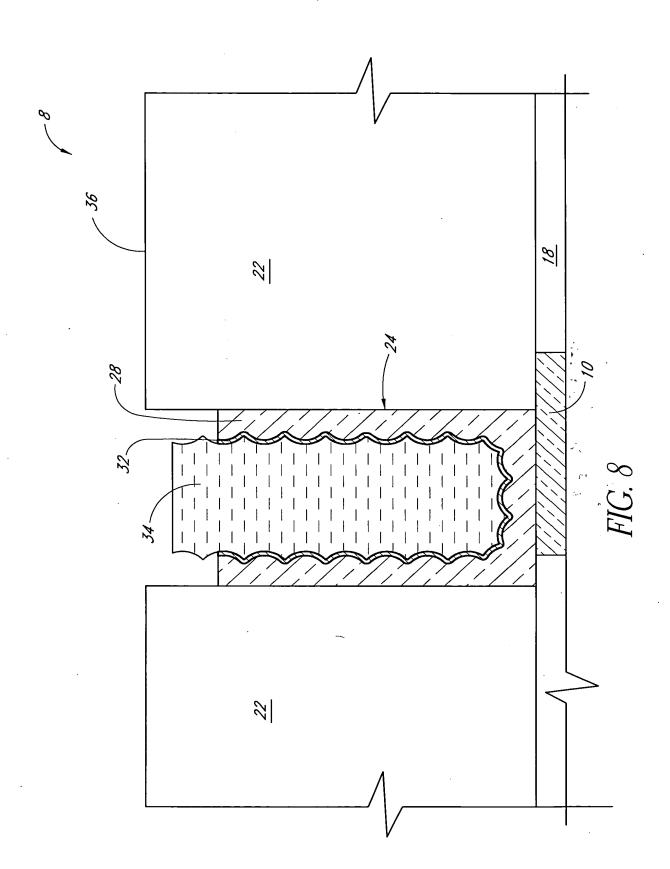


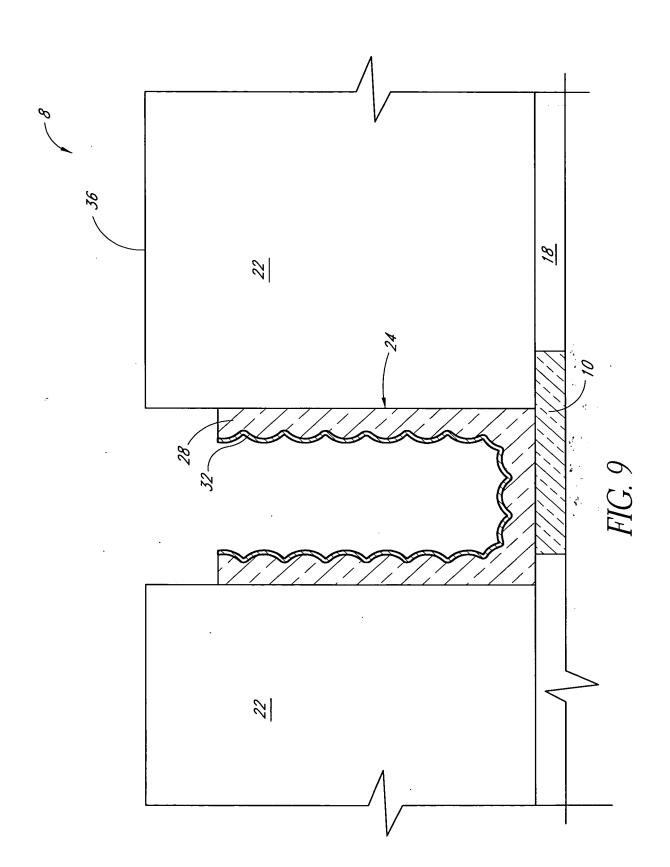
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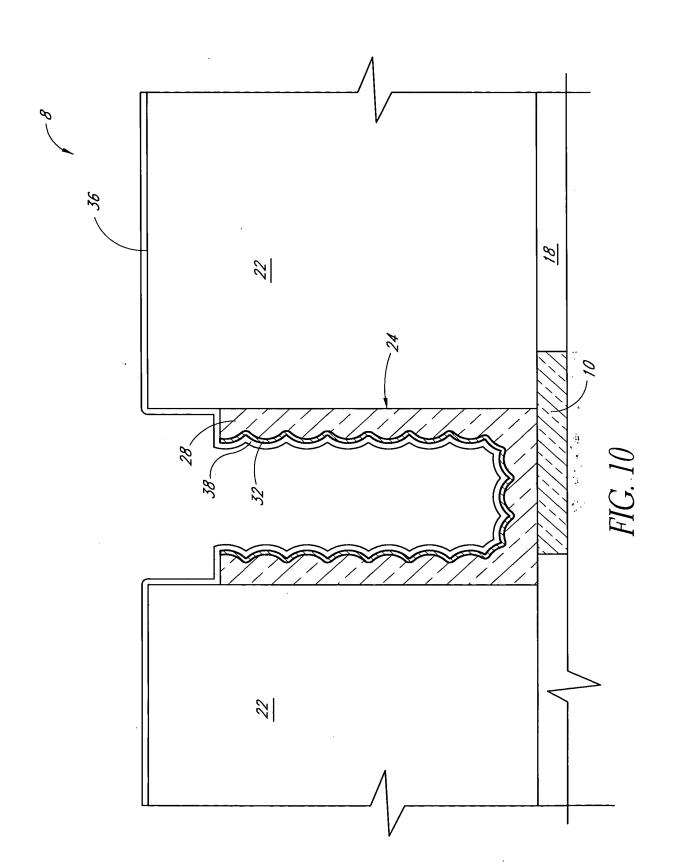


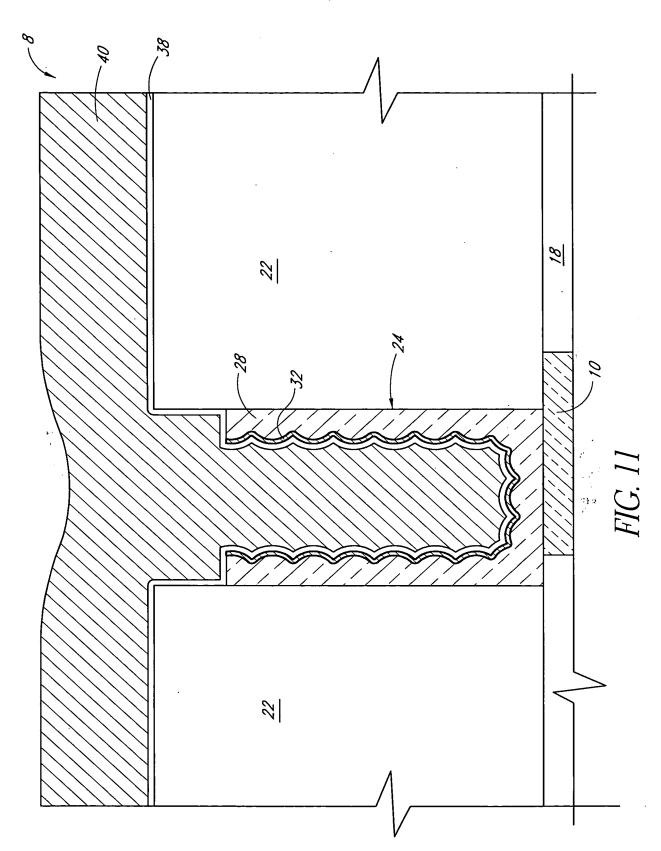


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REDUCED CELL-TO-CELL SHORTING FOR MEMORY ARRAYS

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FIG. 12A

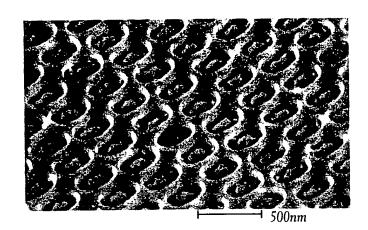


FIG. 12B

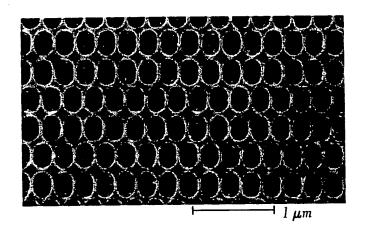
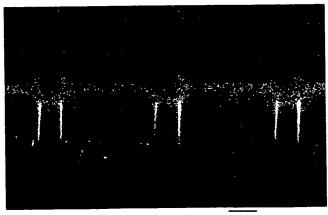


FIG. 12C



`100nm